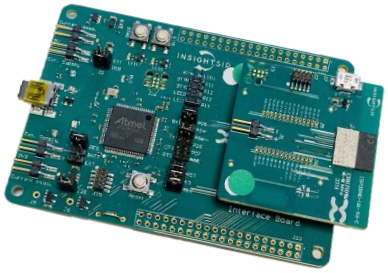
ISP5261 DK
Data Sheet

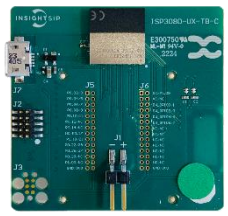
Development Boards

In order to assist clients in developing their WiFi6 connectivity solutions based on the ISP5261, Insight SIP offers associated Test Boards and Evaluation Boards. They provide full HW and SW necessary development environment for product developers to start working on their solutions. Time to market is saved by avoiding starting from a blank sheet of paper.



Test Board

The Test Board consists of a module mounted on a PCB for prototyping and testing. It includes a connection to the Insight SiP Interface Board included in ISP5261 Evaluation Board (see nearby).



It also offers test points for all IOs and can be used in conjunction with an external J-Link programmer.

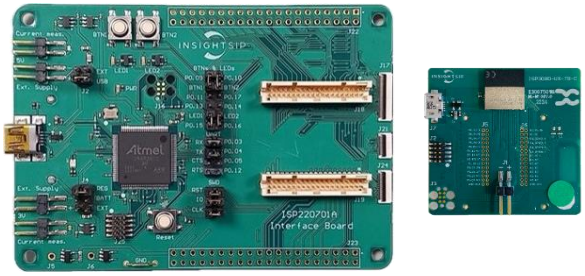
Ordering Information

- ISP5261-WX-TB
  Test Board

Evaluation Board

The Evaluation Board offers the perfect solution to start with ISP5261 combo module. It includes:

- One Interface Board with integrated J-Link OB JTAG/SWD Emulator
- One Test Board ISP5261-WX-TB connected to the Interface Board for testing purpose



Ordering Information

- ISP5261-WX -EB
  Evaluation Board

The information contained in this document is the property of Insight SiP and should not be disclosed to any third party without written permission. Specification subject to change without notice.